

CREATEK Microelectronics

ESD Protection Diode in DFN1006

Features

- 80Watts peak pulse power (tp = 8/20µs)
- DFN1006 package
- Bidirectional configurations
- Low clamping voltage
- Low leakage current
- Solid-state silicon-avalanche technology
- Protection one data/power line to: IEC 61000-4-2 ±15kV contact ±25kV air IEC 61000-4-4 (EFT) 40A (5/50ns) IEC 61000-4-5 (Lightning) 10A (8/20µs)





Mechanical Data

- Case: DFN1006 (plastic package). Lead free; RoHS compliant
- Molding Compound Flammability Rating: UL 94 V-0
- **Terminals:** High temperature soldering guaranteed: 260 °C/10 sec. at terminals

Applications

- Cell Phone Handsets and Accessories
- Microprocessor based equipment
- Personal Digital Assistants (PDA's)
- Notebooks, Desktops, and Servers
- Portable Instrumentation

Absolute Maximum Ratings

Ratings at 25 °C, ambient temperature unless otherwise specified

Parameter	Symbol	Value	Unit
Peak Pulse Power (T _P =8/20μS)	P _{PP}	80	W
ESD contact/air discharge (IEC-61000-4-2)	V _{ESD}	15/25	kV
Peak Pulse Current (t₂ = 8/20µS)	I _{PP}	10	Α
Junction Temperature	TJ	-55 to +125	$^{\circ}$
Storage temperature	T _{STG}	-55 to +125	${\mathbb C}$

Electrical Characteristics

(T_A = 25 °C unless otherwise specified)

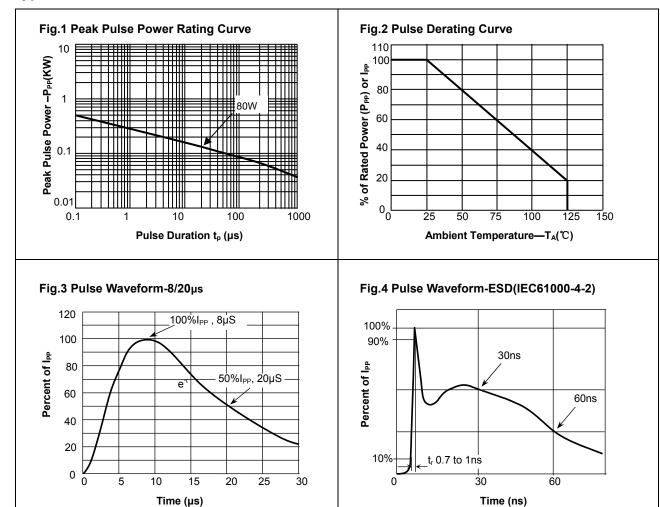
Parameter	Symbol	Conditions	Min	Typical	Max	Units
Reverse Stand-Off Voltage	V_{RWM}				3.3	V
Reverse Breakdown Voltage	V_{BR}	I _T =1mA	4.1			V
Reverse Leakage Current	I _R	V _{RWM} =3.3V,T=25℃		0.1	0.2	μΑ
Clamping Voltage ¹⁾	V_{CL}	I _{PP} =16A,t _p =100ns		8		V
Clamping Voltage ²⁾	.,,	I _{PP} =5A,t _p =8/20μs			6	٧
Clamping Voltage ²⁾	V _C	I _{PP} =10A,t _p =8/20μs			8	V
Dynamic resistance 1)	R _{DYN}			0.2		Ω
Junction Capacitance	C _j	$V_R = 0V, f = 1MHz$		15		pF

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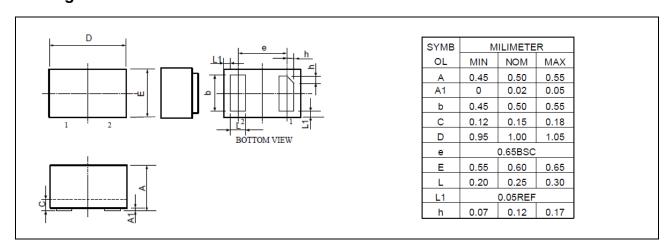


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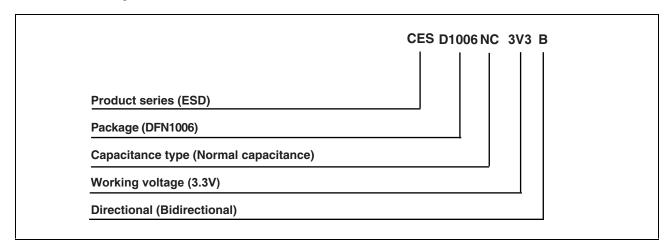
Typical Characteristics (T_{amb} = 25 °C unless otherwise specified)



Package Dimensions



Part number system



Marking

3E

Ordering information

Order code	Package	Packaging option	Base quantity	Packaging specification
CESD1006NC3V3B	DFN1006	Tape and reel	10000pcs / reel	EIA STD RS-481

Revision history

Date	Revision	Changes
23-May-2016	1.0	Initial release

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CESD1006NC3V3B

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