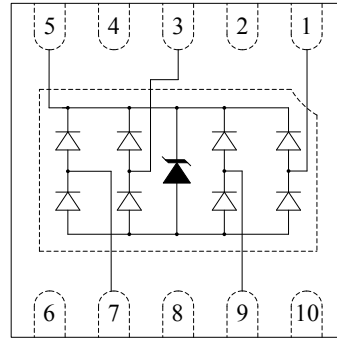


Low Capacitance ESD TVS Array in DFN2626

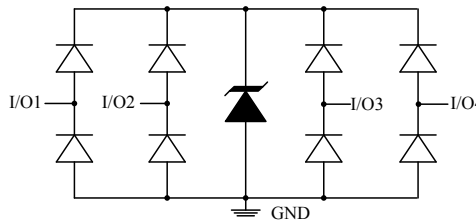
Features

- Transient protection for high-speed data lines
 - IEC 61000-4-2 (ESD) ±30kV (Air)
 - ±30kV (Contact)
 - IEC 61000-4-4 (EFT) 40A (5/50 ns)
 - IEC 61000-4-5 (Surge) 25A (8/20µs)
- Package optimized for high-speed lines
- Provides protection for two line pairs
- Low capacitance: 3.8pF @ 0V (Typical)
- Low leakage current: 0.1µA @ V_{RWM} (Typical)
- Low operating and clamping voltage
- Each I/O pin can withstand over 1000 ESD strikes for ±8kV contact discharge



Mechanical Data

- **Case:** DFN2626 (plastic package).
Lead free; RoHS compliant
- **Molding Compound Flammability Rating:**
UL 94 V-0
- **Terminals:** High temperature soldering guaranteed:
260 °C/10 sec. at terminals



Applications

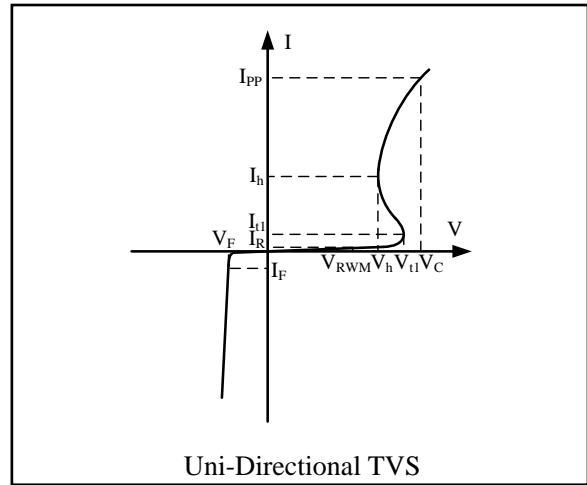
- 10/100/1000M Ethernet Ports
- WAN/LAN Equipment
- Desktops, Servers and Notebooks
- Cellular Phones
- Switching Systems
- Audio/Video Inputs

Absolute Maximum Ratings

Ratings at 25 °C, ambient temperature unless otherwise specified

Parameter	Symbol	Value	Unit
Peak Pulse Power (T _P =8/20µS)	P _{PP}	450	W
ESD contact/air discharge (IEC-61000-4-2)	V _{ESD}	30/30	kV
Peak Pulse Current (t _P = 8/20µS)	I _{PP}	25	A
Junction Temperature	T _J	-55 to +125	°C
Storage temperature	T _{STG}	-55 to +150	°C

Symbol	Parameter
V_{RWM}	Nominal Reverse Working Voltage
I_R	Reverse Leakage Current @ V_{RWM}
V_{t1}	Trigger Voltage
I_{t1}	Trigger Current @ V_{t1}
V_h	Holding Voltage
I_h	Holding Current @ V_h
V_C	Clamping Voltage @ I_{PP}
I_{PP}	Maximum Peak Pulse Current
C_{ESD}	Parasitic Capacitance

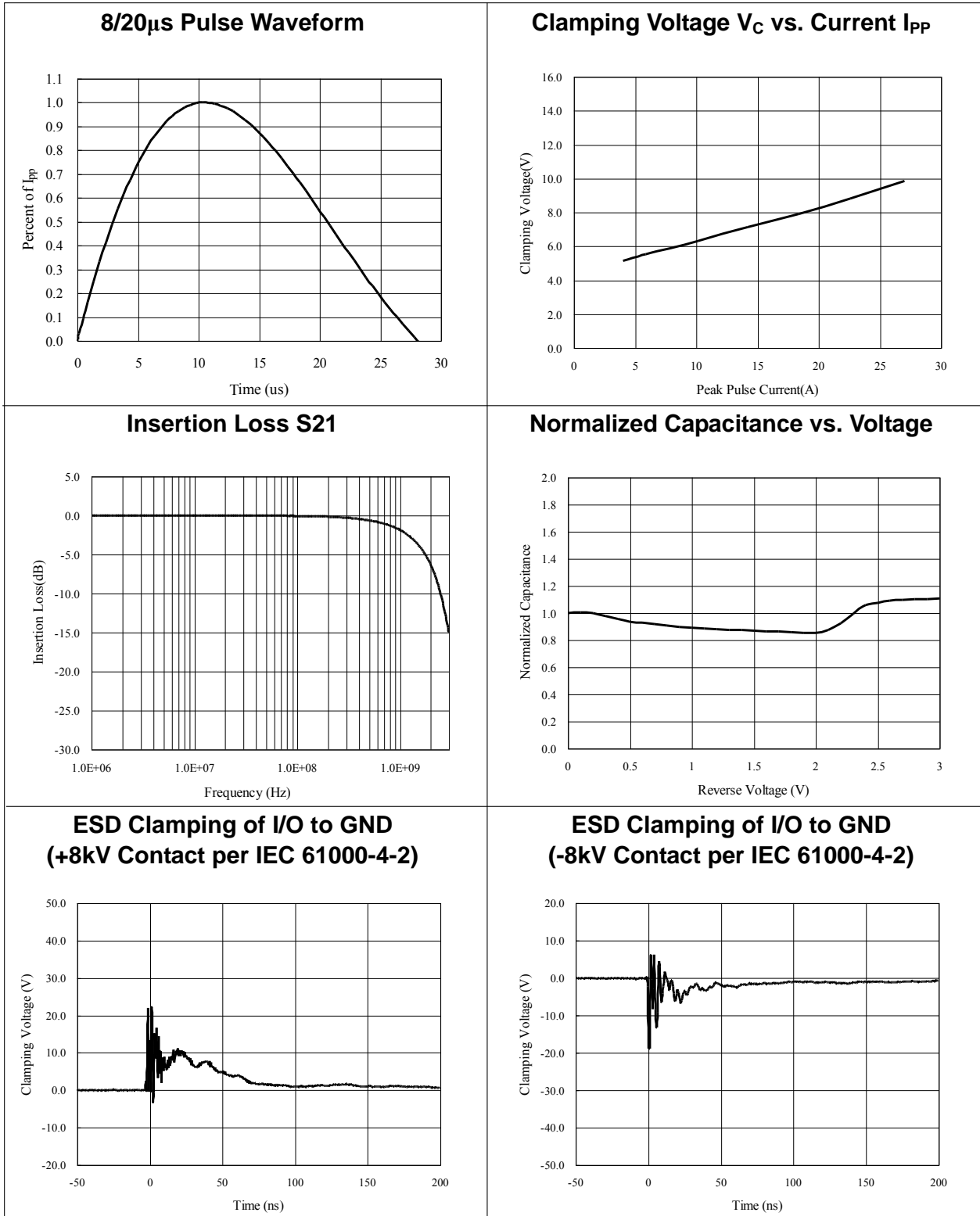


Electrical Characteristics

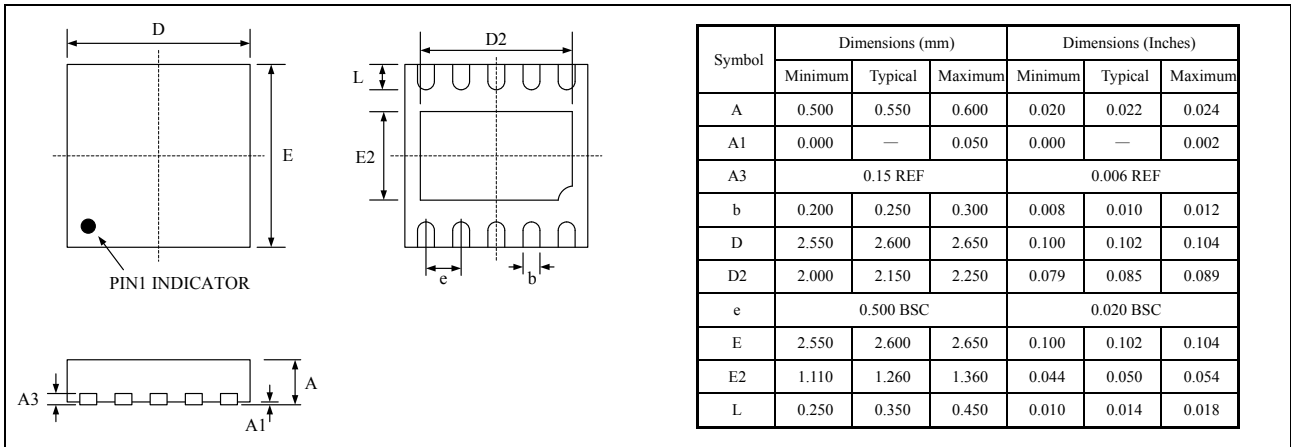
($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Test Condition	Minimum	Typical	Maximum	Units
V_{RWM}				3.3	V
I_R	$V_{RWM} = 3.3\text{V}, T = 25^\circ\text{C}$		0.1	1.0	μA
V_{t1}	$I_{t1} = 1\mu\text{A}$	3.5	4.2	5.0	V
V_h	$I_h = 1\text{mA}$	3.3		4.5	V
V_C	$I_{PP} = 1\text{A}, t_p = 8/20\mu\text{s}$ (Each Line)			5.5	V
V_C	$I_{PP} = 10\text{A}, t_p = 8/20\mu\text{s}$ (Each Line)			8.5	V
V_C	$I_{PP} = 25\text{A}, t_p = 8/20\mu\text{s}$ (Each Line)			16.0	V
C_{ESD}	Between I/O Pins and Ground $V_R = 0\text{V}, f = 1\text{MHz}$		3.8	5.0	pF
C_{ESD}	Between I/O Pins $V_R = 0\text{V}, f = 1\text{MHz}$		2.0	2.5	pF

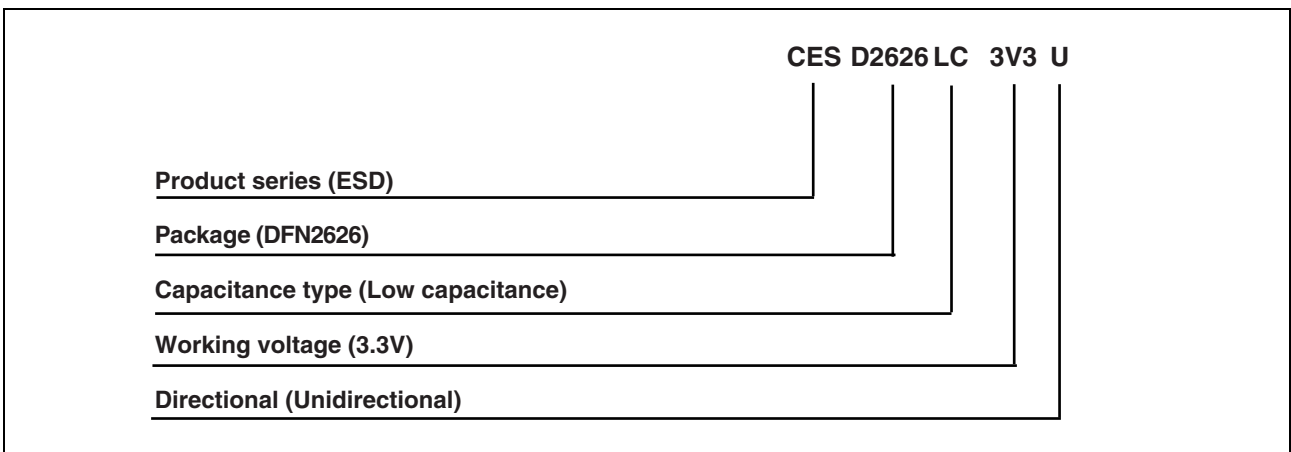
Typical Characteristics ($T_{amb} = 25\text{ }^{\circ}\text{C}$ unless otherwise specified)



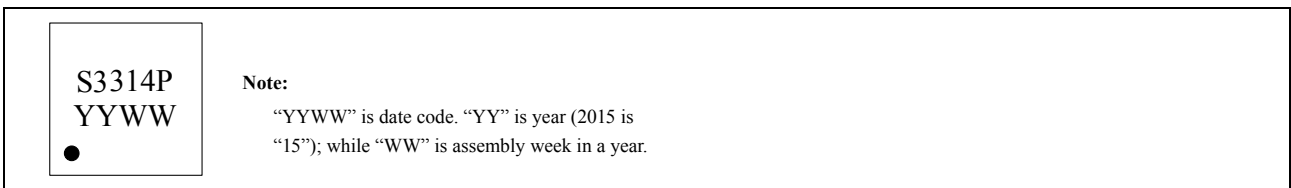
Package Dimensions



Part number system



Marking



Ordering information

Order code	Package	Packaging option	Base quantity	Packaging specification
CESD2626LC3V3U	DFN2626	Tape and reel	3000pcs / reel	EIA STD RS-481

Revision history

Date	Revision	Changes
23-May-2012	1.0	Initial release

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